**Homework 1**

**Assembly Language**

1. Do the following binary and hex subtractions by two’s complement addition.
2. 10110100 – 10010111
3. FE0Fh – 12ABh
4. Give the unsigned and signed decimal interpretations of each of the following 16-bit or 8-bit numbers.
5. 7FFEh
6. FEh
7. A memory location has a physical address 4A37Bh. Compute
8. the offset address if the segment number is 40FFh.
9. the segment number if the offset address is 123Bh.